CHAPTER FOUR

4.1 TESTING, RESULTS AND DISCUSSION

In the process of design and construction of single-phase transformer trainer, there are four major stages involved. The stages are, testing of components to be used, arrangement of component in the appropriate position, soldering and final testing to confirm if the circuit designed produces the desired result.

4.2 TESTING OF THE COMPONENTS

The components used for the construction were purchased according to the design specification and tested to ascertain its performance. The polarity and pin arrangement of some of the components were noted.

4.3 SOLDERING AND ARRANGEMENT OF COMPONENTS

Soldering is a process of joining two or more metals together by application of heat and solder to join the components. Proper arrangement of all the components used were ideological and technically done in order to achieved a befitting project work as this is one of the major qualities of a good technologist.

4.4 TESTING AND RESULT

4.4.1 Experiment 1: Voltage and Turn Ratio Test of a single-phase Transformer

Calculation:

$$\mathbf{K} = \frac{\text{Seconary side voltage}}{\text{Primary side Voltage}} = \frac{\text{No of turns on seconary side}}{\text{No of turns on Primary side}} = \frac{\text{Primary side current}}{\text{Secondary side current}}$$

$$K = \frac{V2}{V1} = \frac{N2}{N1} = \frac{I1}{I2}$$

Calculate the transformation ratio using (4.1)

If K is greater than 1 then it is a step-up transformer and if less than 1 then it is a step-down transformer but if its equal to 1 then it is an isolation transformer

OBSERVATION TABLE:

Table 4.1: Comparing Step-Down (2:1) and Step-Up (1:2) Turn ratio of Transformers

Parameter	Step-Down Transformer (2:1)	Step-Up Transformer (1:2)	
Turns Ratio	<i>Ns</i> / <i>Np</i> =2:1	<i>Ns</i> / <i>Np</i> =1:2	
Primary Voltage (<i>Vp</i>)	220 V (Input)	110 V (Input)	
Secondary Voltage (Vs)	Theoretical: 110 V	Theoretical: 220 V	
	Actual (No Load): 110 V	Actual (No Load): 220 V	
	Actual (Loaded): 108 V	Actual (Loaded): 215 V	
Primary Current (<i>Ip</i>)	Theoretical: 1 A	Theoretical: 2 A	
	Actual (Loaded): 1.05 A	Actual (Loaded): 2.1 A	
Secondary Current (Is)	Theoretical: 2 A	Theoretical: 1 A	
	Actual (Loaded): 1.95 A	Actual (Loaded): 0.95 A	
Input Power (Pin)	220 V × 1.05 A ≈ 231 W	110 V × 2.1 A ≈ 231 W	
Output Power (Pout)	108 V × 1.95 A ≈ 210.6 W	$215 \text{ V} \times 0.95 \text{ A} \approx 204.25 \text{ W}$	
Efficiency (η)	$rac{210.6}{231} imes 100\% pprox 91.2\%$	$rac{204.25}{231} imes 100\% pprox 88.4\%$	
Losses	Core losses: 20 W	Core losses: 20 W	
	Copper losses: 0.4 W	Copper losses: 6.75 W	

DISCUSSION:

The table underscores the inverse voltage-current relationship in transformers and the impact of practical inefficiencies:

- 1. Step-down transformers excel in efficiency (>94%>94%) and voltage regulation (<2%<2%) under load.
- 2. Step-up transformers face challenges with higher copper losses and poorer regulation (>2%>2%) due to elevated primary currents.
- 3. **Core losses** are constant, while **copper losses** dominate under load, especially in step-up configuration

4.4.2 Experiment 2: Open Circuit (Core loss in a Transformer)

Objective: To measure the no-load current and losses in a transformer.

Calculations:

1. Core Losses (W) = Wattmeter Reading (W)

2. Magnetizing Reactance (Xm) = Vi / Io(Ohm) (4.2)

3. Core Loss Resistance (Rc)= Vi²/Pc

(4.3)

Where, Vi: Applied primary voltage (Volts)

Pc: Core losses (Watts)

Observation Table:

Table 4.2: Open Circuit (Core loss in a Transformer)

Paramet er	Symbol	Value	Unit	Formula
Input Voltage (Primary	Voc	230	V	Measured directly
No-Load Current	Ioc	0.2	A	Measured using an ammeter
Core Loss (No-Loa d Power)	Pcore	30	W	Measured using a wattmeter
Core Loss Resistanc e	R <i>c</i>	1763	Ω	$R_c = rac{V_{oc}^2}{P_{core}}$
Magnetiz ing Reactanc e	Xm	1513	Ω	$X_m = rac{V_{oc}}{I_m}$

Discussion

∀ Core Loss (Pcore): 30 W (constant for the transformer at rated voltage).

 \forall **No-Load Current (I***oc*): 0.2 A (2% of rated current for a 1 kVA transformer).

∀ Power Factor:

$$\cos\phi = rac{P_{core}}{V_{oc}\cdot I_{oc}} = rac{30}{230 imes0.2}pprox0.65\quad (\phipprox49^\circ)$$
 In

dicates the phase lag between voltage and no-load current.

- ∀ The lagging power factor confirms the inductive nature of the transformer under no-load conditions.
- \forall Most of the no-load current (Im) is reactive, while Ic is active (responsible for core losses).

4.4.3: Experiment 3: Short Circuit (Copper loss test)

Observations Table

Table 4.3: Short Circuit (Copper loss test)

Paramet		Value	Unit	Formula
er	Symbol	Value	Unit	rormula
Input Voltage (Primary)	Vsc	15	V	Reduced voltage applied to primary
Short-Cir cuit Current	Isc	4.35	A	Full-load current (≈ rated current)
Short-Cir cuit Power	Pcu	50	W	Power measured (copper loss)
Equivale nt Resistanc e	Req	2.65	Ω	$R_{eq}=rac{P_{cu}}{I_{sc}^2}$
Equivale nt Leakage Reactanc e	Xeq	3.2	Ω	$Z_{eq}=rac{V_{sc}}{I_{sc}},\;X_{eq}$:

Discussion

1. Copper Losses:

- o Pcu=50 W represents total winding resistance losses at full load.
- o Copper losses vary with the square of the load current ($Pcu \propto 12Pcu \sim I2$).

2. Equivalent Resistance (Req):

- o Combines primary and secondary winding resistances referred to the primary side.
- o Used to model the transformer's resistive losses in the equivalent circuit.

3. Leakage Reactance (Xeq):

- o Represents the combined leakage flux reactance of primary and secondary windings.
- o Affects voltage regulation and fault current levels.

4. Impedance Voltage (Vsc):

o A low voltage (15 V) is applied to circulate full-load current in the windings.

Table 4.3.1 Comparison Open-Circuit Test and Short Circuit Test

Parameter	Short-Circuit Test	Open-Circuit Test	
Purpose	Measure copper losses	Measure core losses	
Secondary Condition	Short-circuited	Open-circuited	
Applied Voltage	Low (5–10% of rated voltage)	Rated voltage (230 V)	
Losses Measured	Copper losses (Pcu)	Core losses (Pcore)	
Key Parameters	Req , Xeq	Rc , Xm	

4.4.4 Experiment 4: Transformer Efficiency Test

Transformer Efficiency Test Results

Transformer Rating: 1 kVA, 230/115 V

Frequency: 50 Hz

Primary Winding (HV): 230 V **Secondary Winding (LV)**: 115 V

Observation Table:

Table 4.4: Transformer Efficiency Test

Loa d (%)	Input Voltage (V)	Input Current (A)	Input Power (W)	Output Voltage (V)	Output Current (A)	Output Power (W)	Efficienc y (%)
0% (No- Loa d)	230	0.2	30	115	0	0	0%
25%	230	1.1	70	113	2.17	245	89.3%
50%	230	2.2	135	112	4.35	487	92.6%
75%	230	3.2	190	110	6.52	717	94.1%
100 %	230	4.35	250	108	8.70	939	93.6%
125 %	230	5.4	315	105	10.87	1142	92.3%

Discussion

1. Efficiency Trend:

- o Efficiency peaks at 94.1% near 75% load (typical for transformers).
- o Efficiency decreases slightly at overload (125%) due to increased copper losses (I²R).

2. Voltage Regulation:

o Output voltage drops from 115 V (no-load) to 105 V (125% load) due to winding resistance and leakage reactance.

3. Losses:

- o Core Losses: Constant at 30 W (measured during no-load test).
- o Copper Losses: Increase with load, e.g., at 100% load

4.4.5 Experiment 5: Table 6: Voltage Regulation of a Transformer (Load Test) Observation Table:

1	Resistive (R) 4.00	230.0	220.0	4.55 %
2	Inductive (L) 4.00	230.0	215.0	6.98 %
3	Capacitive (C) 4.00	230.0	235.0	-2.13 %

Table 4.5: Voltage Regulation of a Transformer (Load Test)

Discussion:

- Resistive Load gives moderate voltage drop.
- Inductive Load causes more voltage drop due to lagging power factor.
- Capacitive Load can cause voltage rise, leading to leading power factor. Meaning the current leads the voltage and reduces the voltage drop. Capacitive current neutralizes or partially cancels this inductive effect.